



219.38760X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Dominic J. GASBARRO et al.

Serial No.: 09/731,746

Filed: December 8, 2000

For: HOST-FABRIC ADAPTER HAVING BANDWIDTH-  
OPTIMIZING, AREA-MINIMAL, VERTICAL SLICED  
MEMORY ARCHITECTURE AND METHOD OF CONNECTING A  
HOST SYSTEM TO A CHANNEL-BASED SWITCHED FABRIC  
IN A DATA NETWORK

Art Unit: Unknown

Examiner: Not assigned

05/29/2001 TV0111 00000020 09731746

01 FC:103

54.00 OP

PRELIMINARY AMENDMENT

Assistant Commissioner of Patents  
Washington, DC 20231

January 17, 2001

Adjustment date: 05/29/2001 TV0111  
01/18/2001 HNDOR1: 00000069 09731746  
02 FC:103

72.00 OP

Prior to examination, please amend the above-identified  
application as follows:

IN THE CLAIMS:

Please add new claims 21-24 as follows:

Refund Ref:  
05/29/2001

0030004997

1 --21. A method of storing data consisting of multiple widths  
Credit Card Refund Total: \$18.00

2 and multiple occurrences of data widths in a memory, comprising:  
Am Exp.: XXXXXXXXXXXX1099

3 grouping the data widths of the same size and designating

4 as registers;

5 determining the number of registers;

6 selecting a number of vertically arranged memory slices of

7 registers of different sizes based on register width and system

01/18/2001 HNDOR1 00000069 09731746

01 FC:102  
02 FC:103

80.00 OP  
72.00 OP

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